JC872 U.S. PTO
09/900945
07/10/01

Class	Subclass

ISSUE CLASSIFICATION

PATENT NUMBER

(4)

U.S. UTILITY Patent Application

O.I.P.E.	PATENT DATE
SCANNED KCC3 Q.A. LA	

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/900945	F	714		2184	

APPLICANTS: Toshitada Saito

TRIMMINGS

TITLE: One-chip system large-scale integrated circuit including processor circuit and its peripheral circuits

PTO-2040
12/99

ISSUING CLASSIFICATION

ORIGINAL		CROSS REFERENCE(S)	
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)
INTERNATIONAL CLASSIFICATION			
			☐ Continued on Issue Slip Inside File Jacket

☐ TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
☐ The term of this patent subsequent to _____ (date) has been disclaimed.	(Assistant Examiner) (Date)			NOTICE OF ALLOWANCE MAILED	
☐ The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____	(Primary Examiner) (Date)			ISSUE FEE	
				Amount Due	Date Paid
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